FIG.1

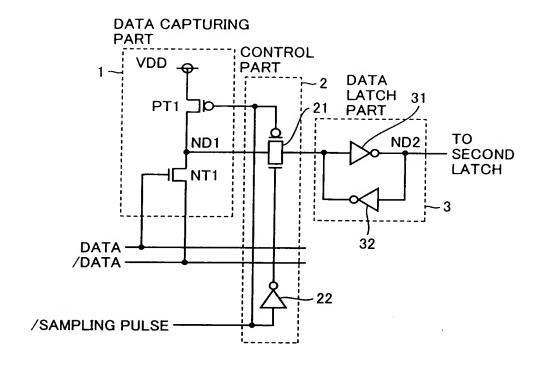


FIG.2

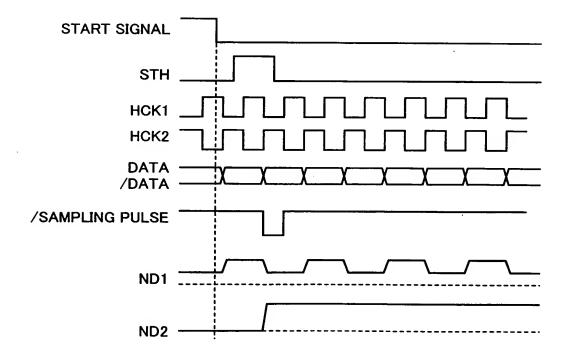


FIG.3

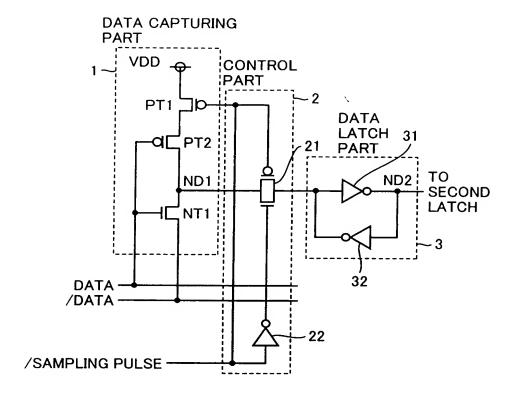


FIG.4

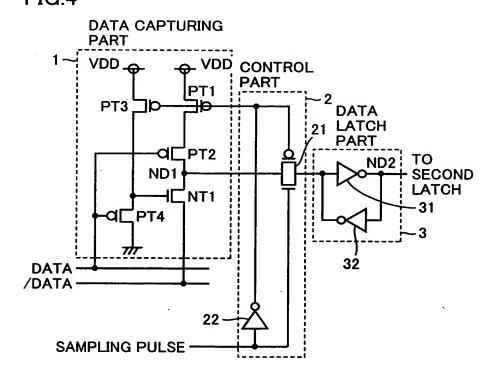


FIG.5

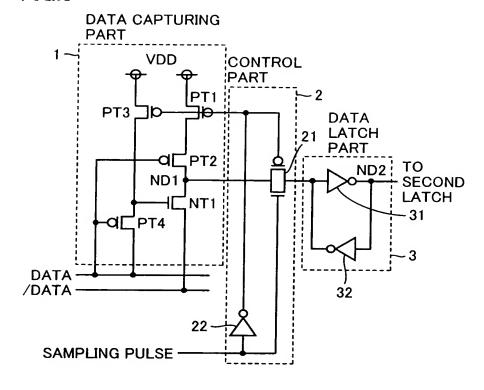


FIG.6

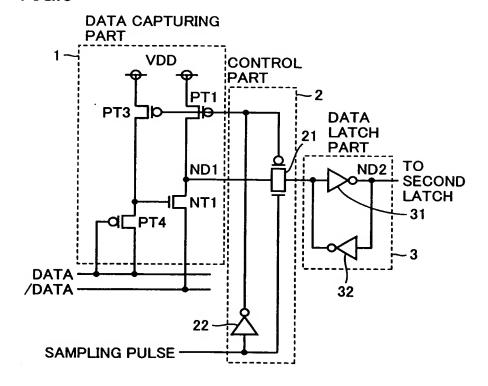
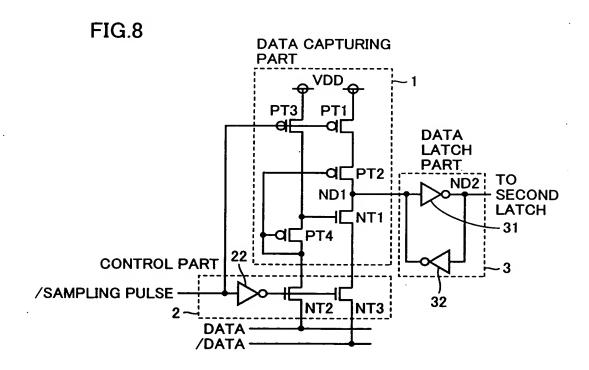


FIG.7 DATA CAPTURING **PART** ⊕-VDD-⊕ PT3 PT1 DATA LATCH **PART** a∐PT2 TO ND2 ND1 SECOND 22 **LATCH** H∐NT1 **CONTROL PART** - 31 PT4 SAMPLING PULSE -32 NT2 NT3 DATA /DATA



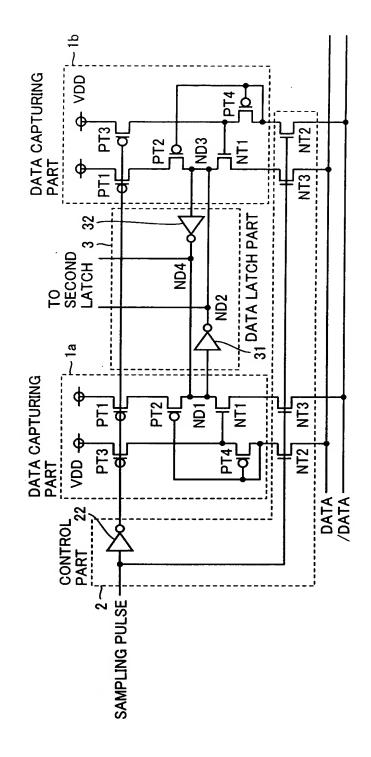


FIG.9

FIG.10 PRIOR ART

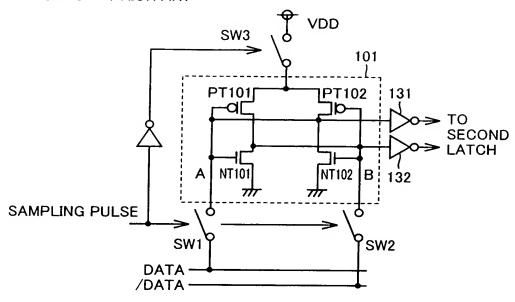


FIG.11 PRIOR ART

